

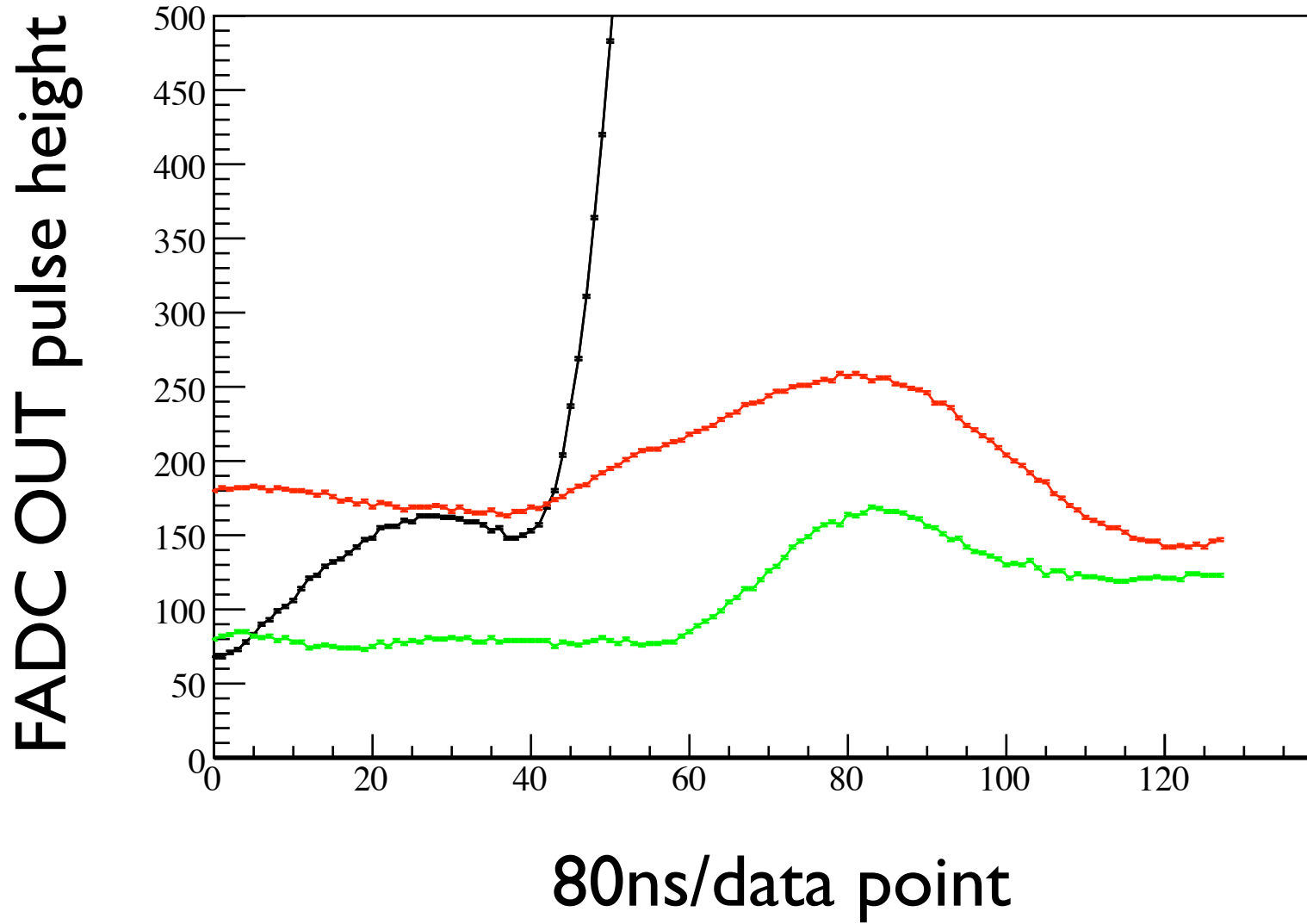
27/Nov/2006 H.Tatsuno

FADC data
pedestal figures for checking common mode

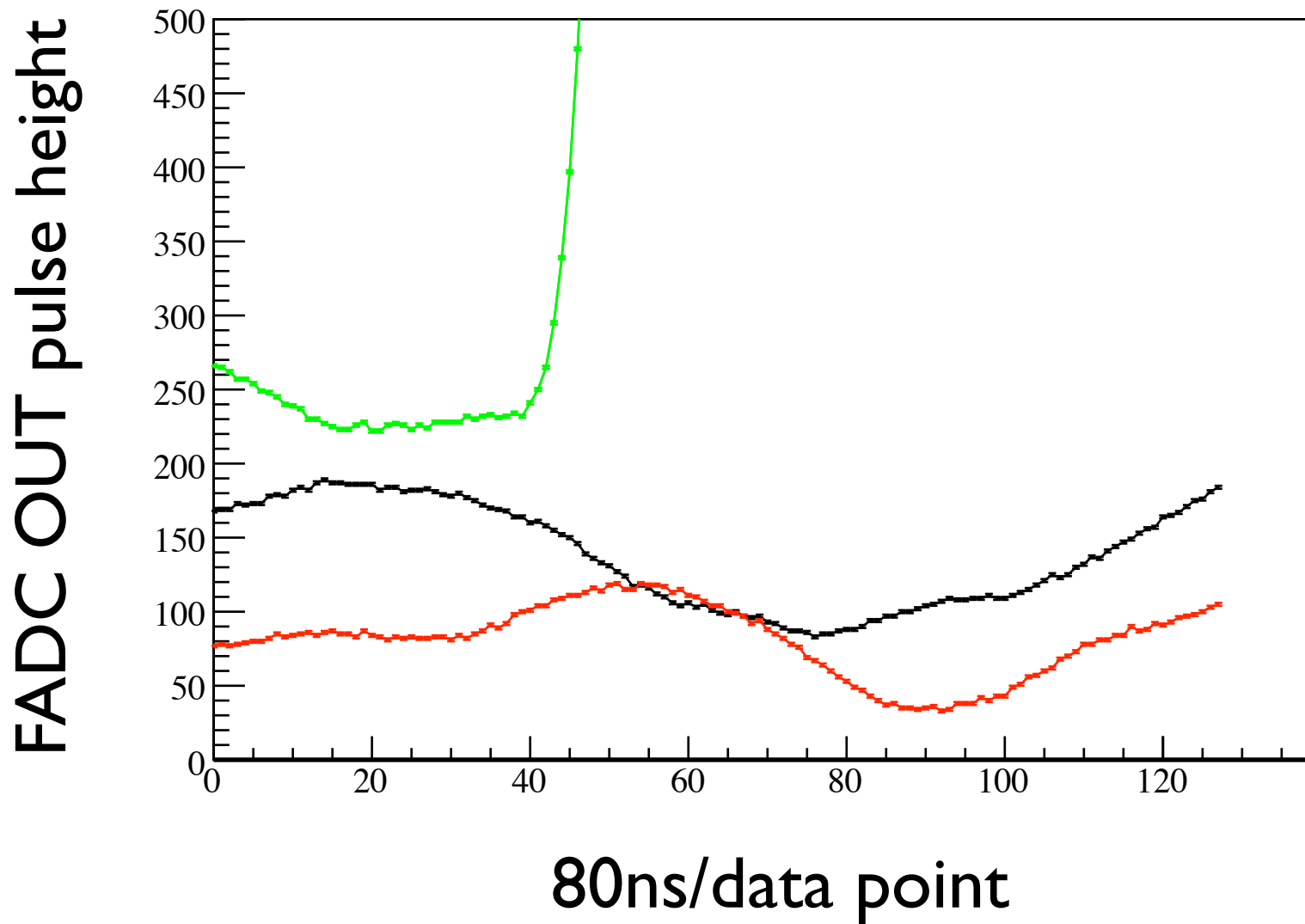
Summary

The common mode structure can be seen,
but it is difficult to correct (or subtract)
the pedestal of a signal systematically.

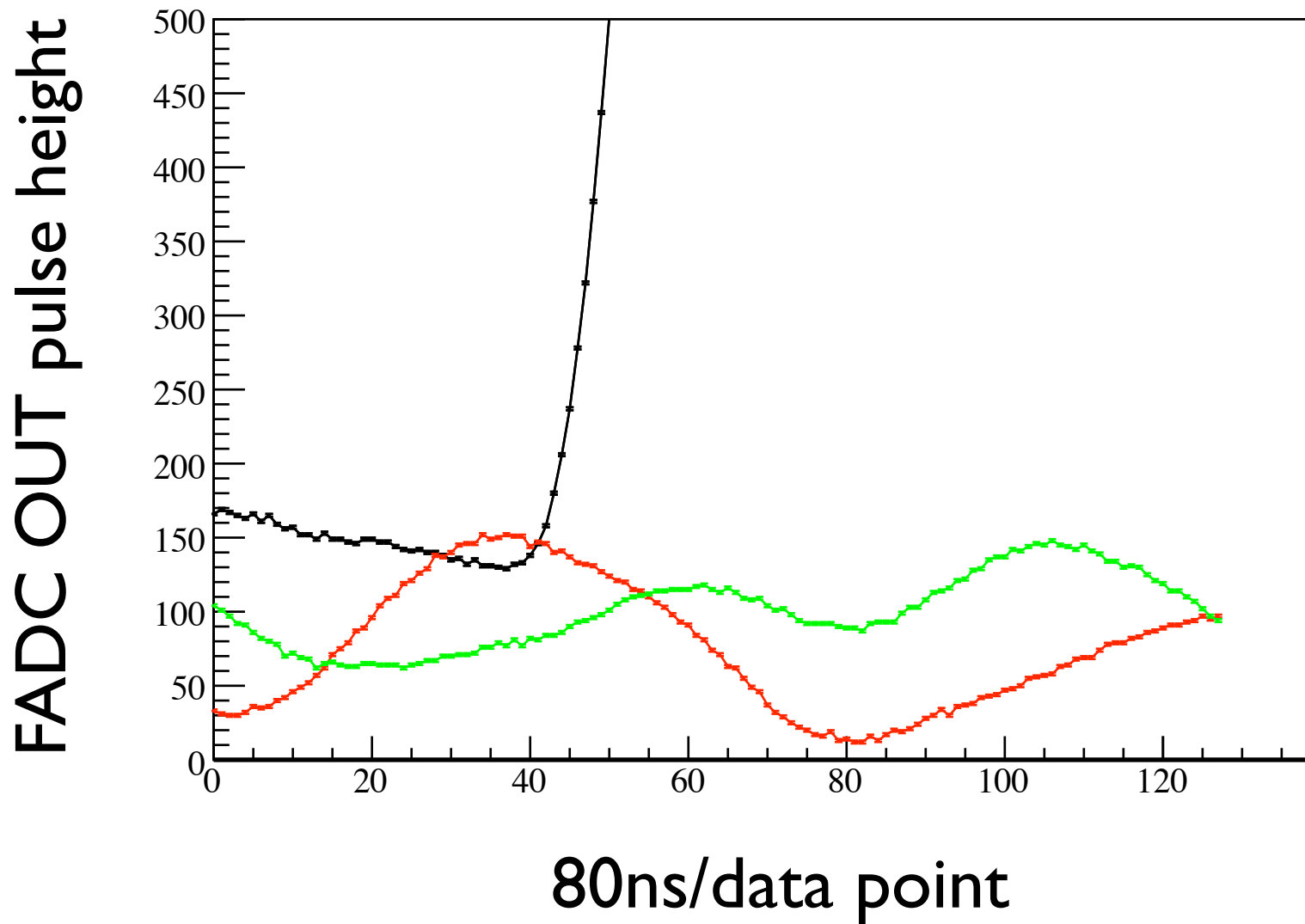
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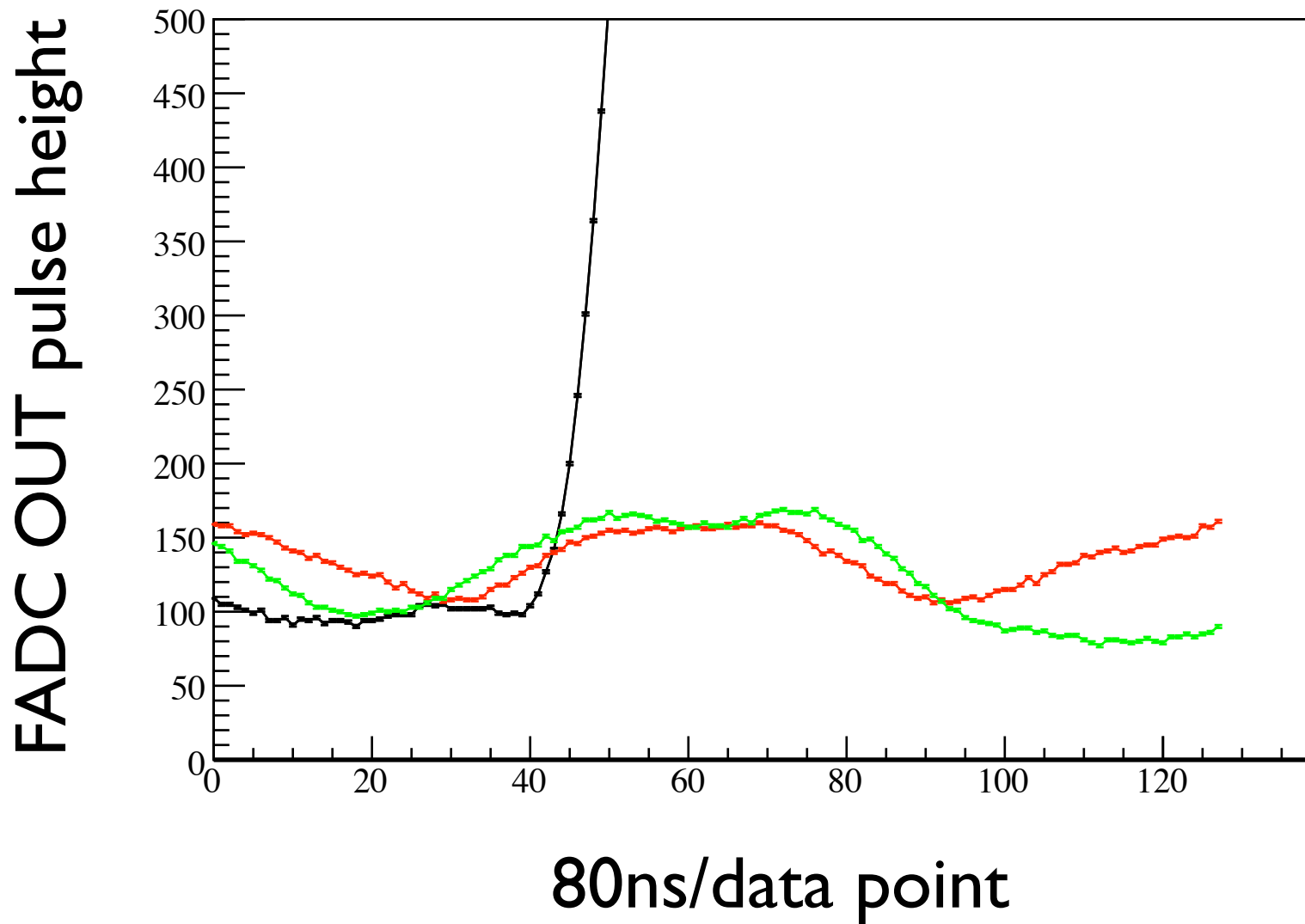
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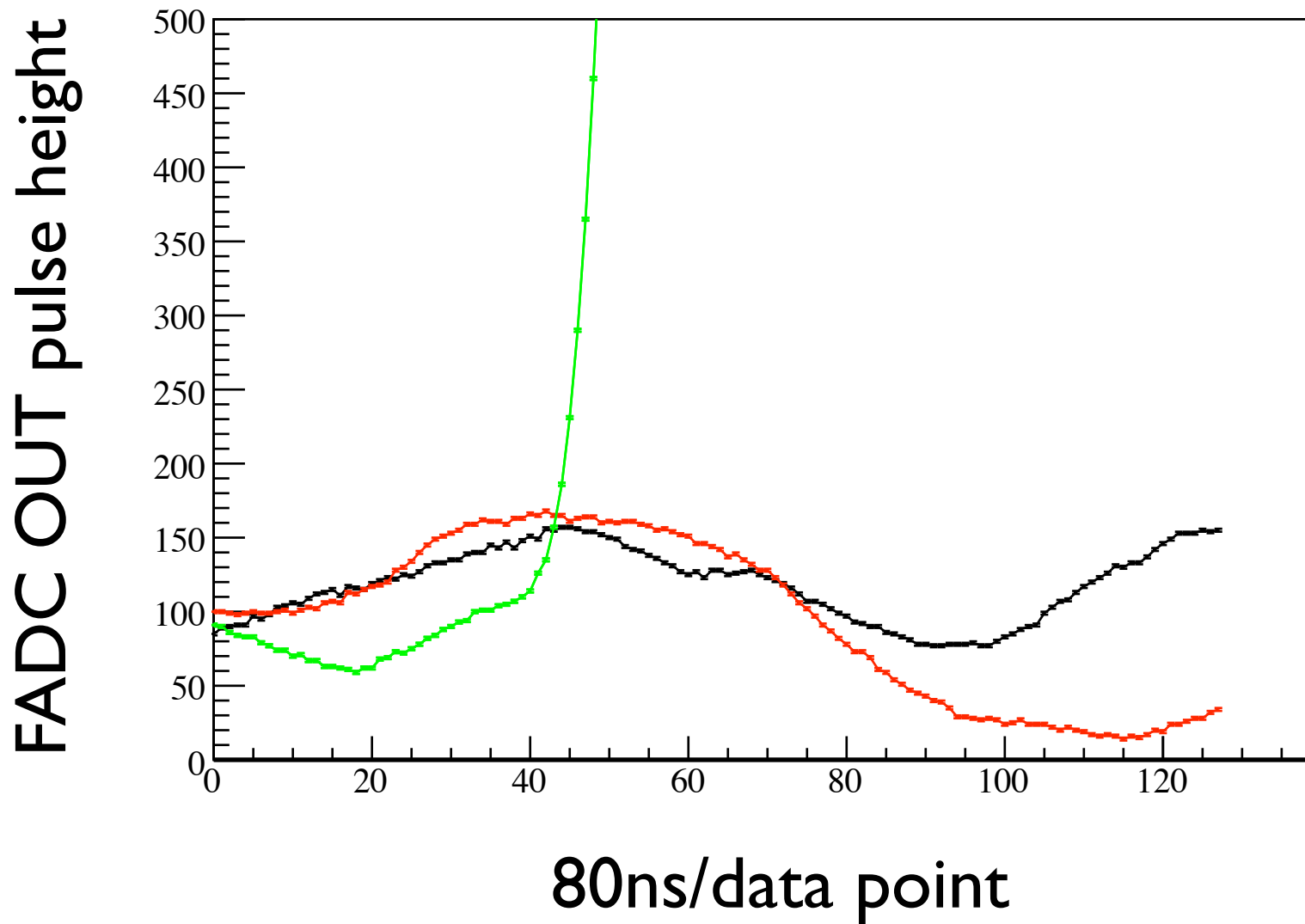
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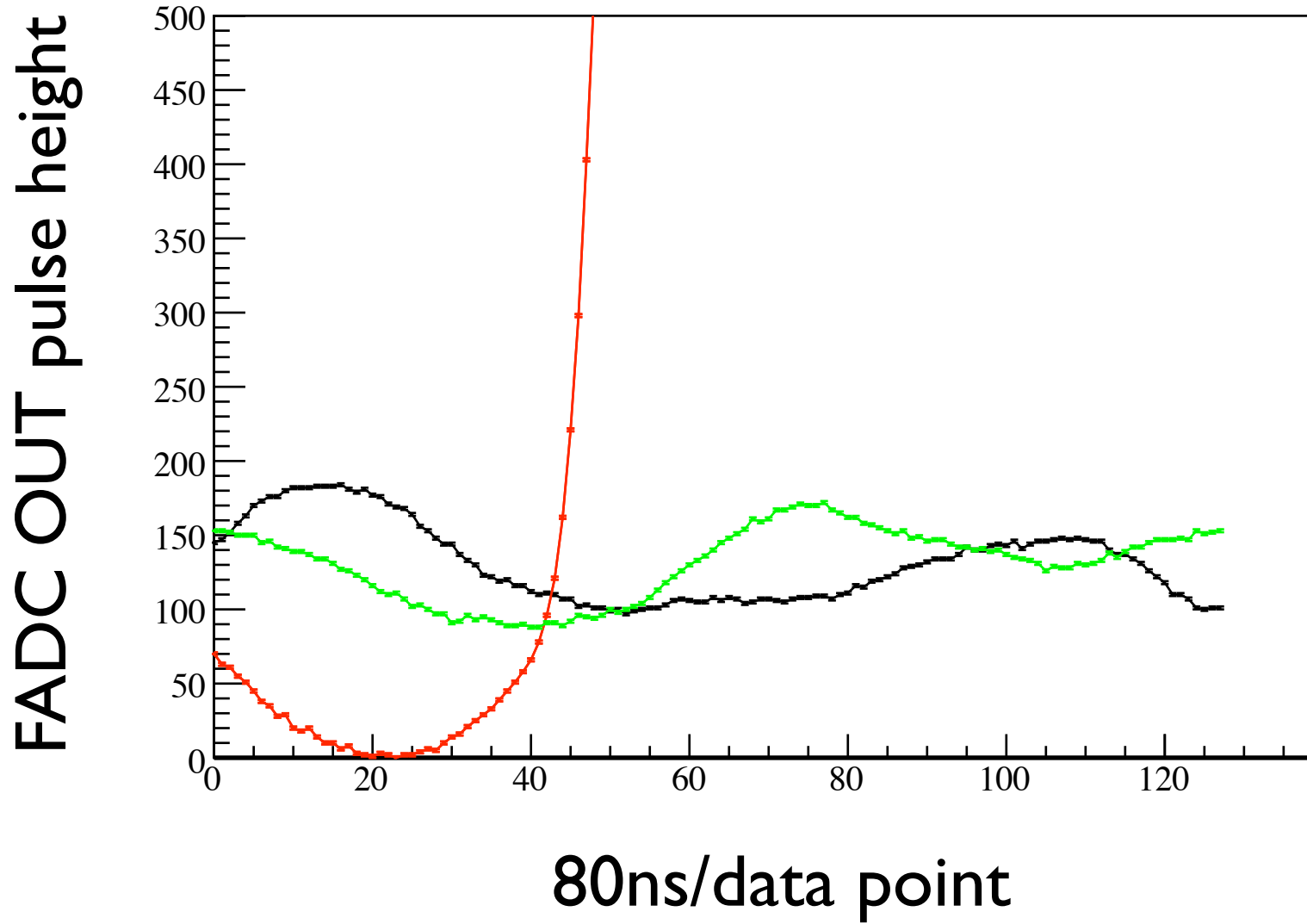
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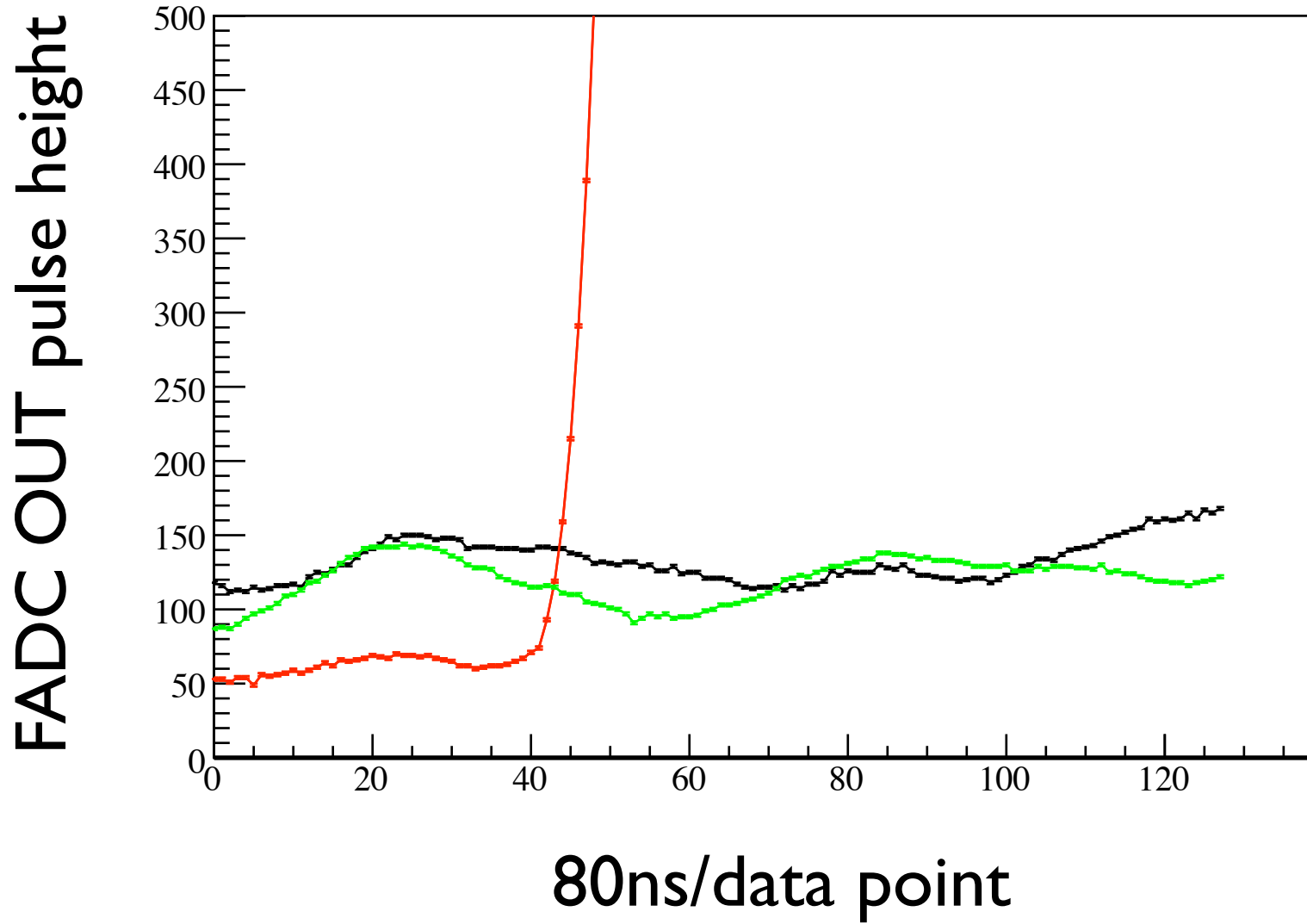
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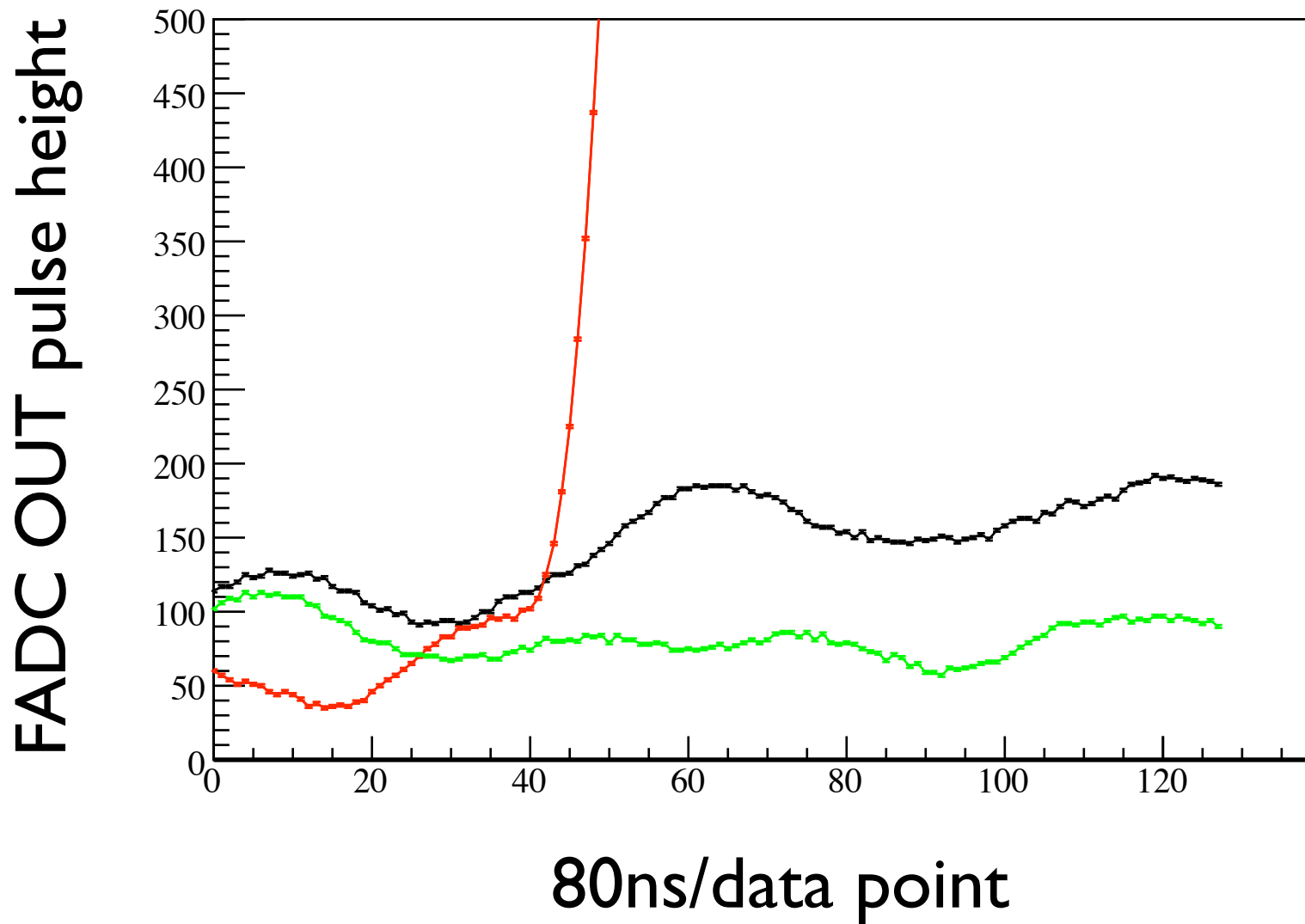
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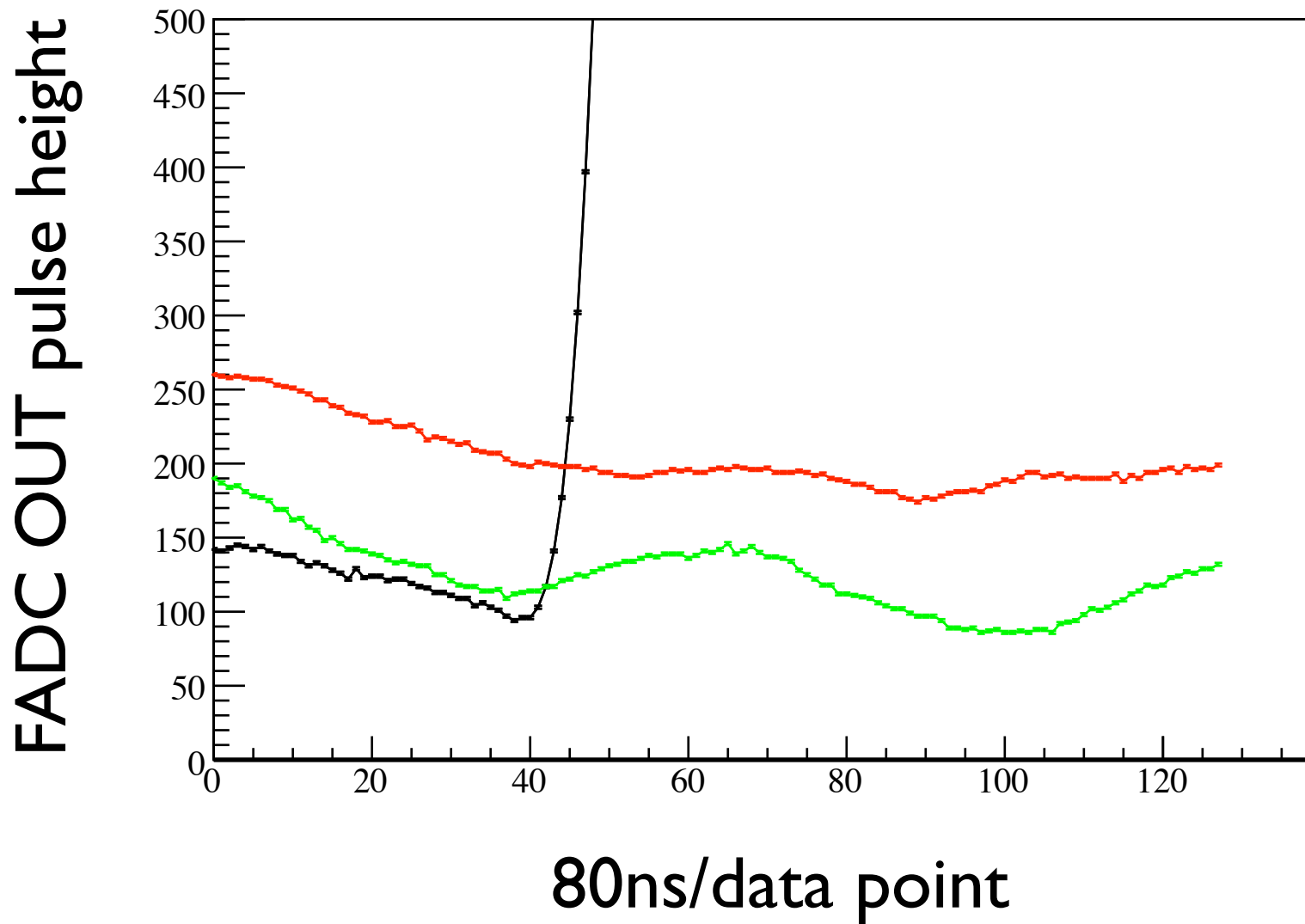
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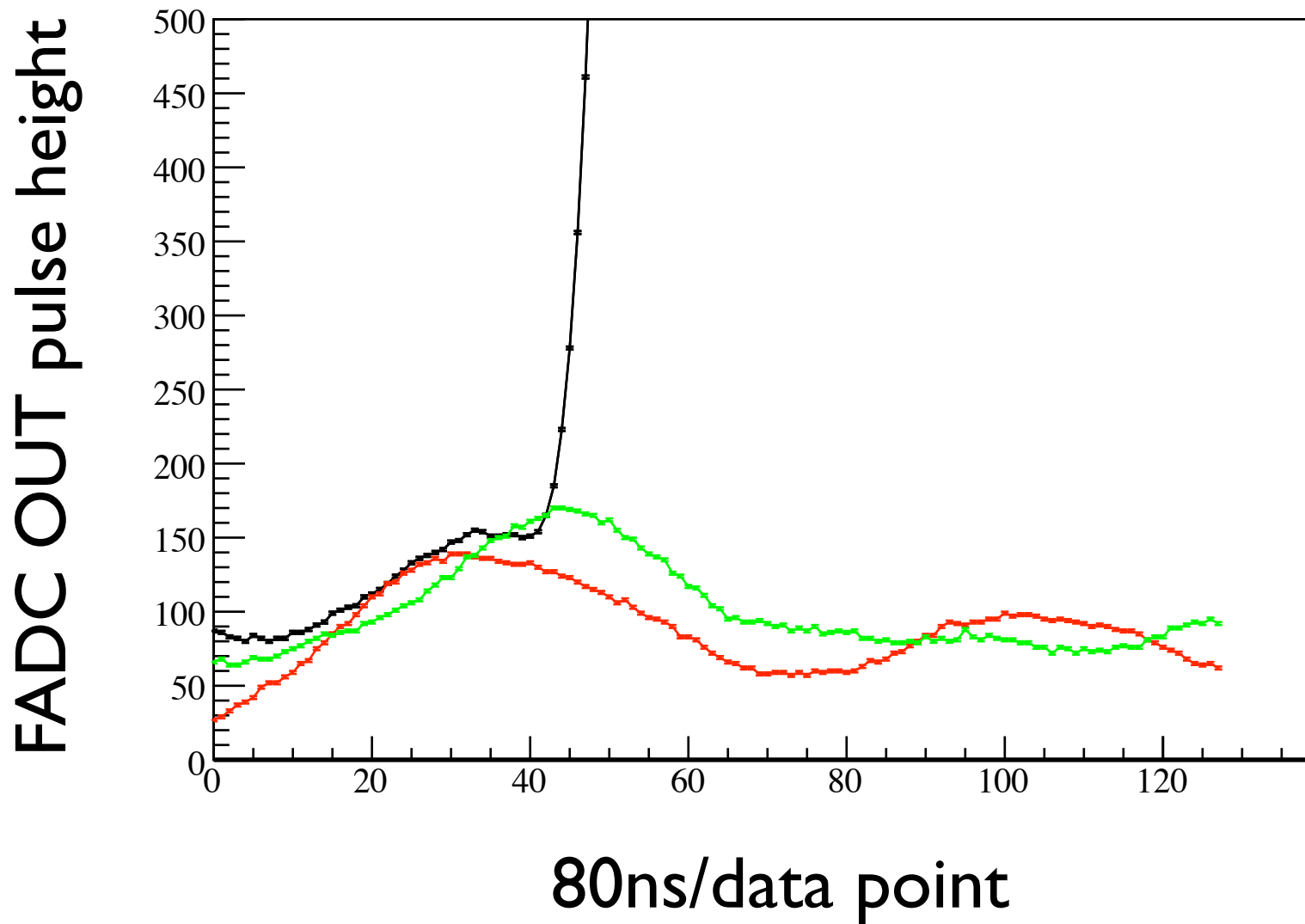
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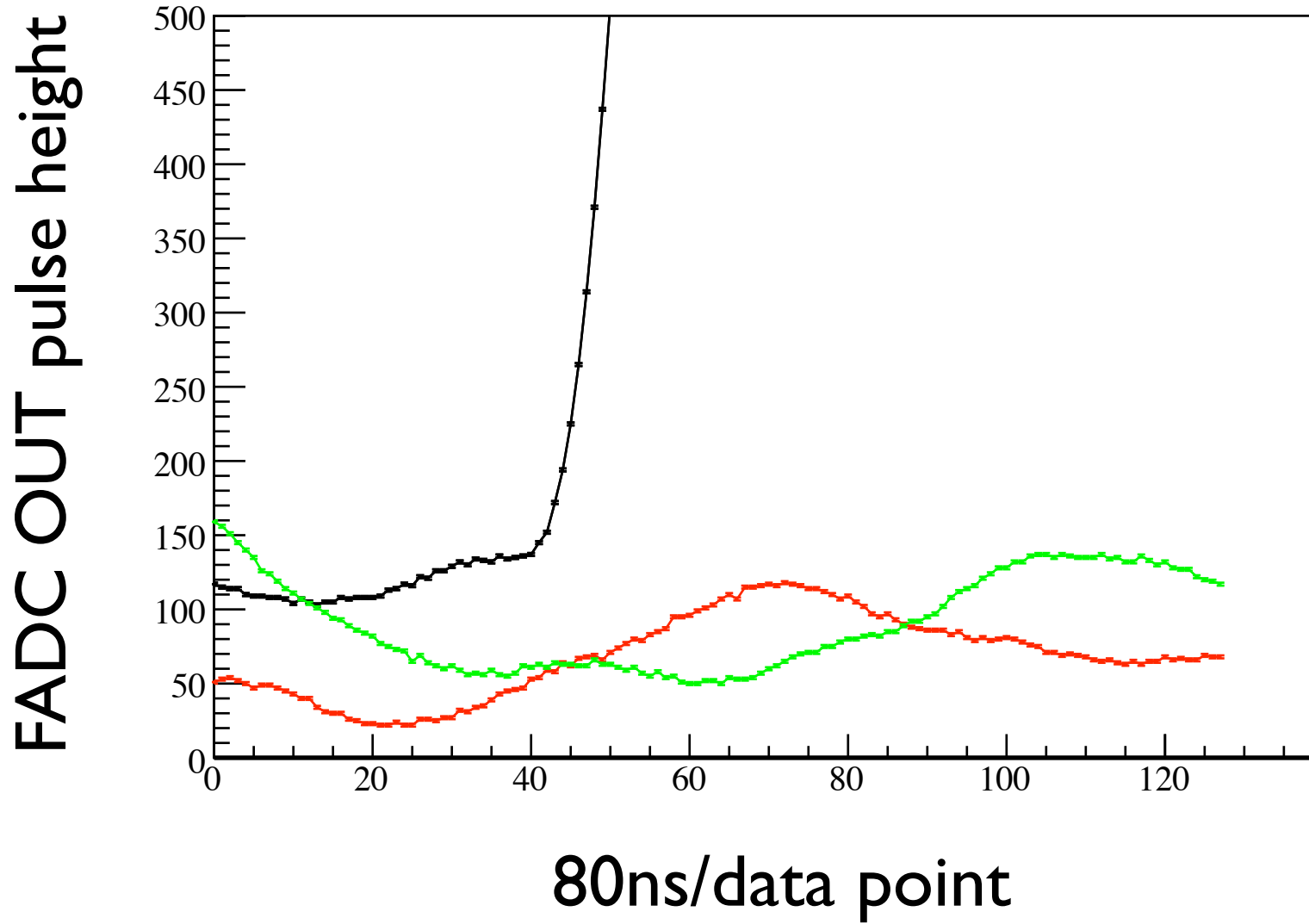
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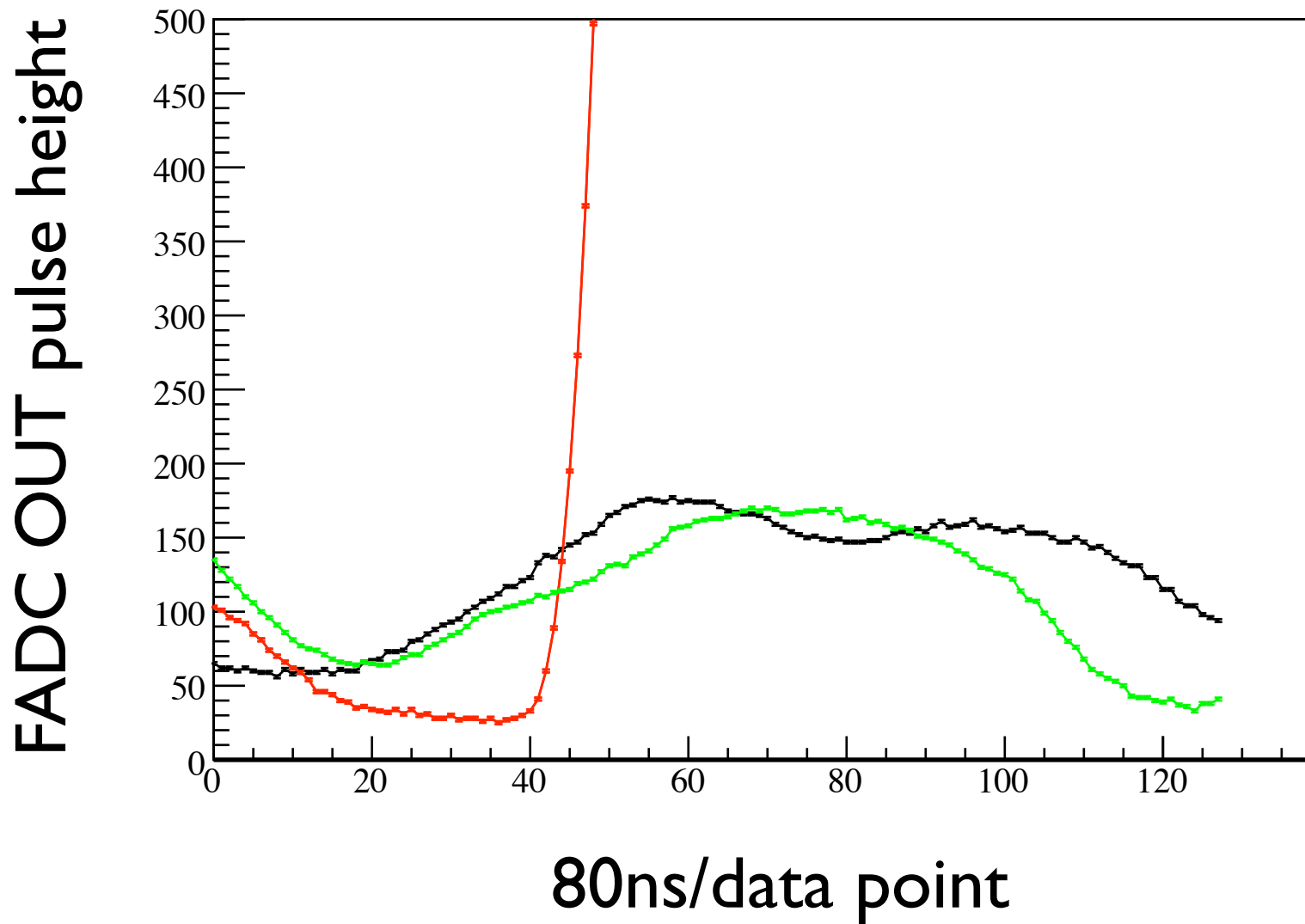
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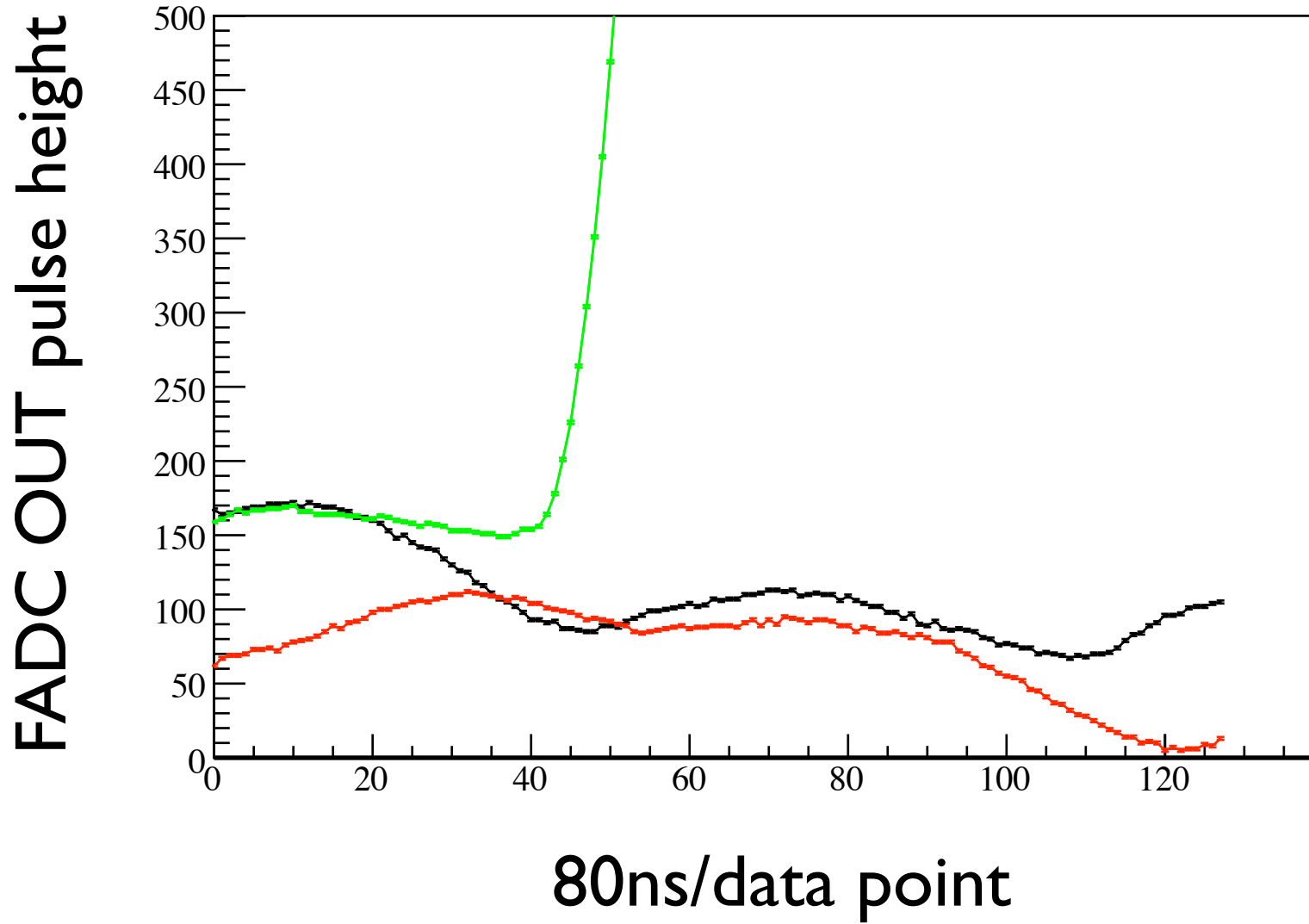
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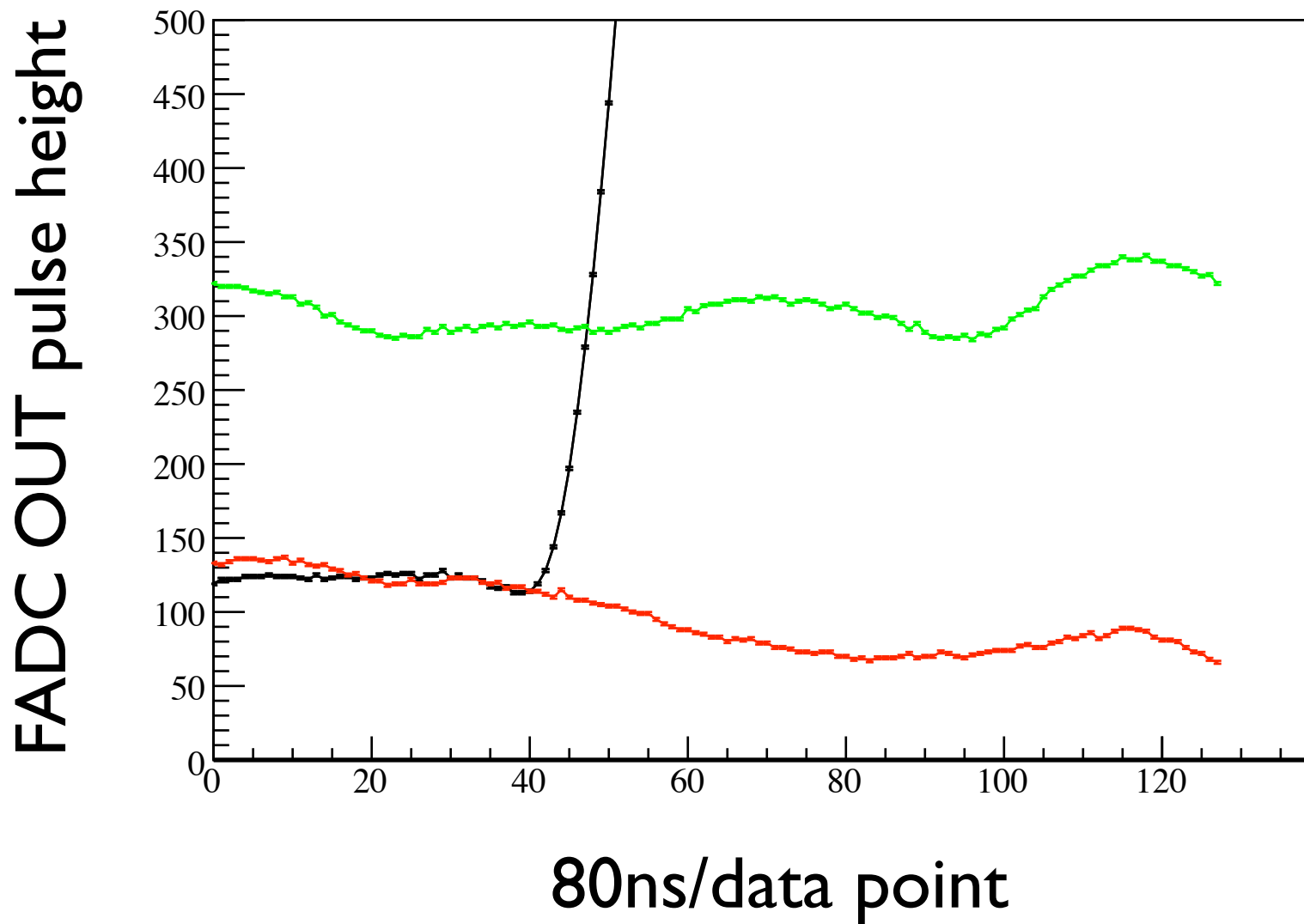
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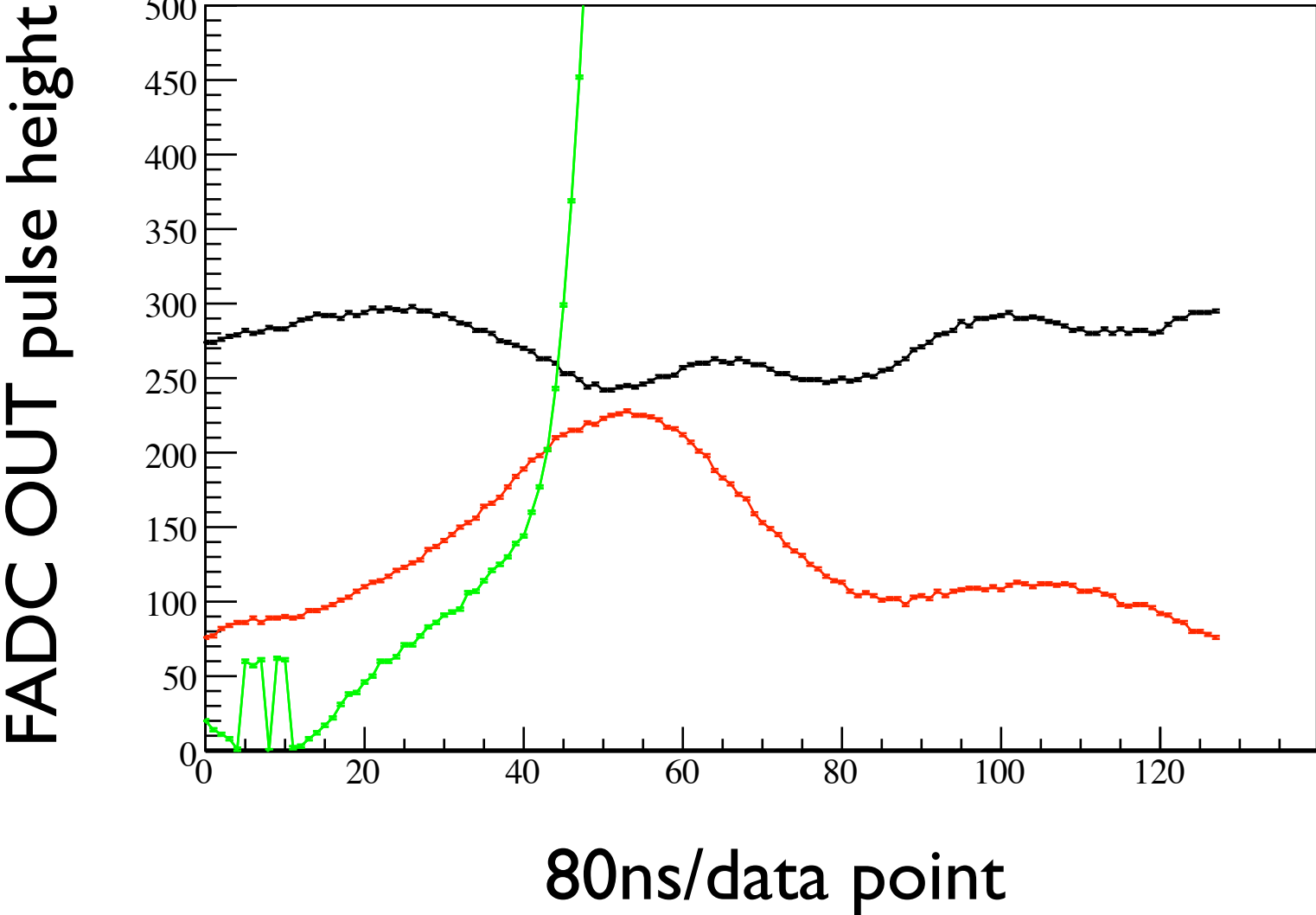
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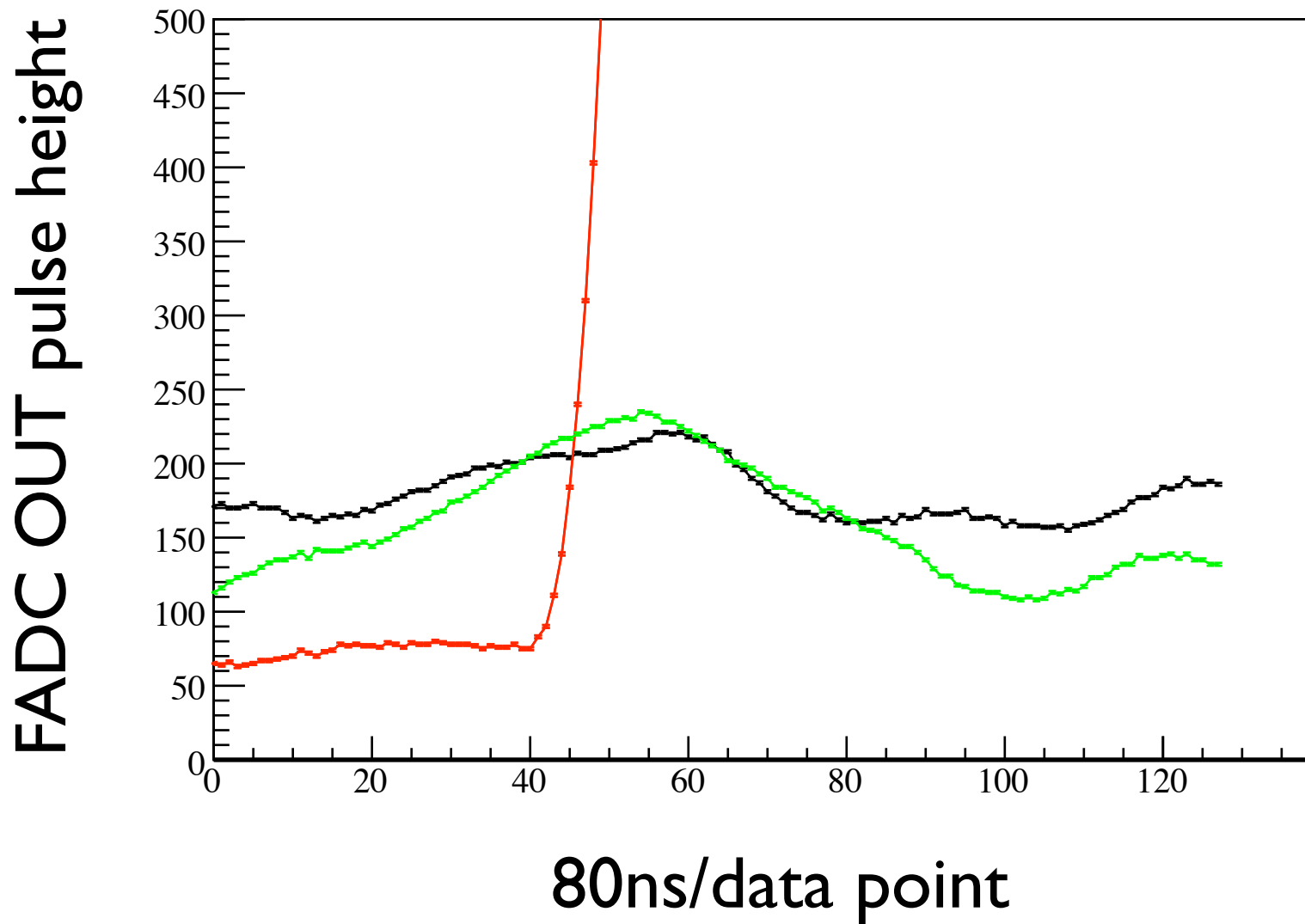
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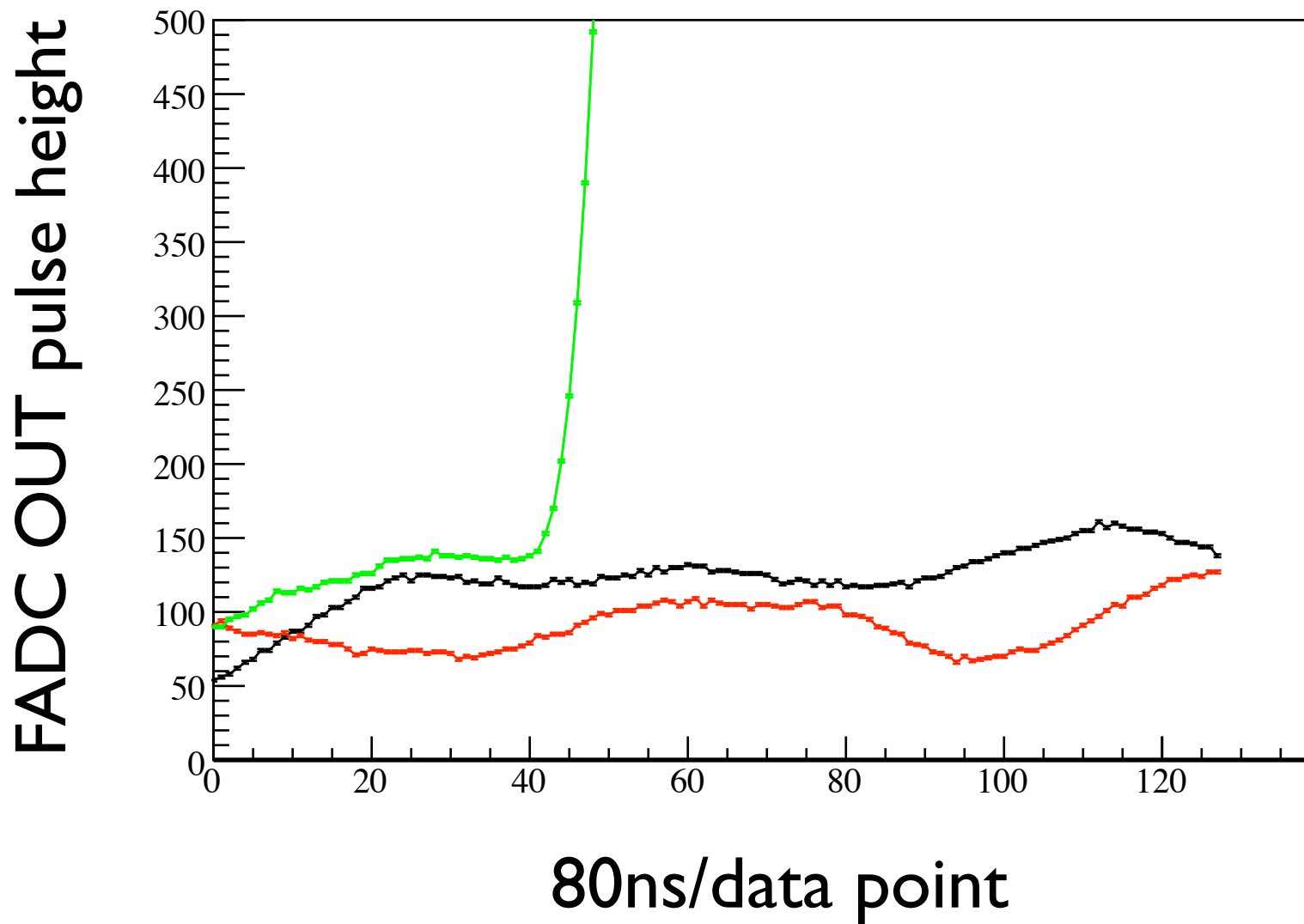
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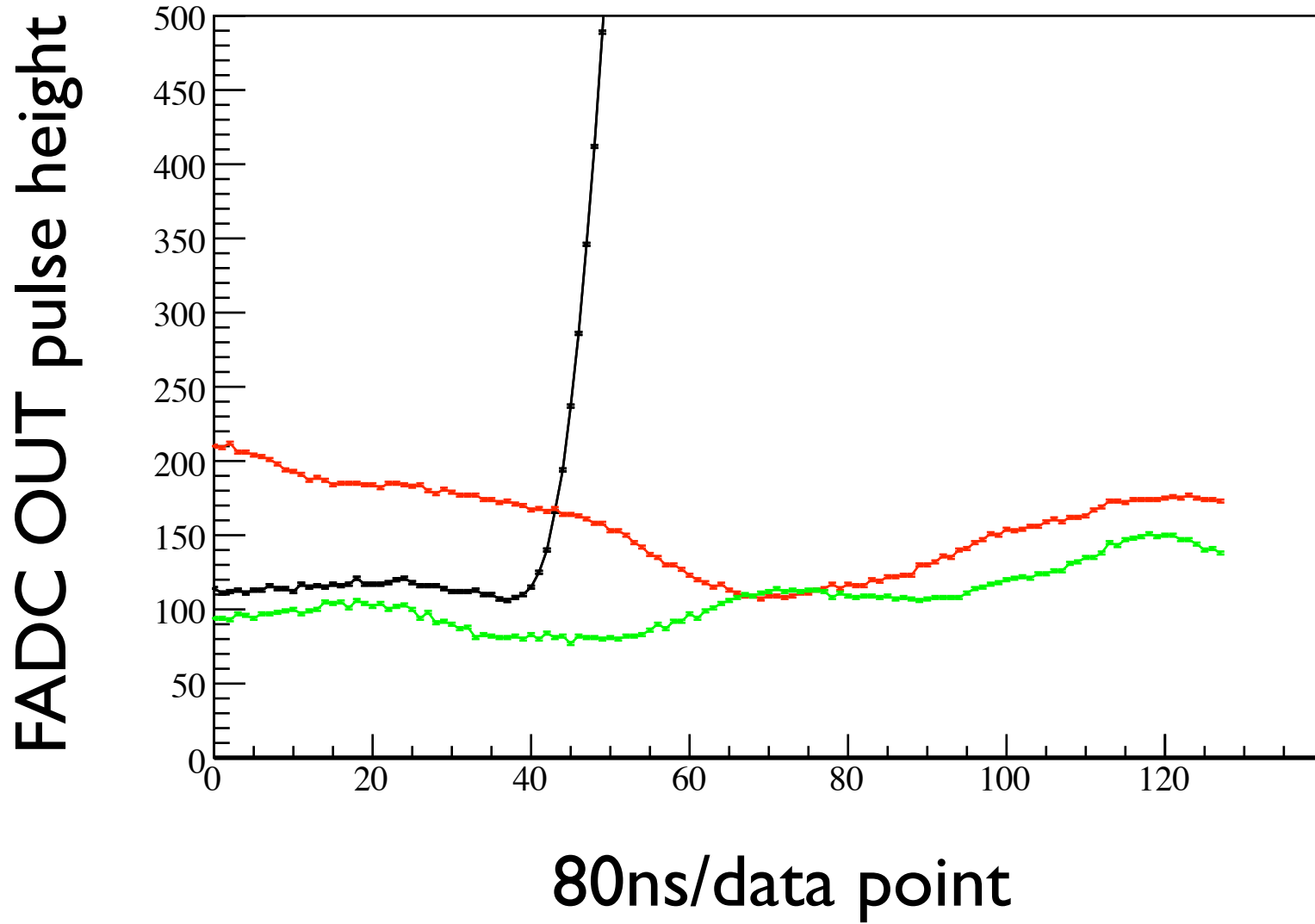
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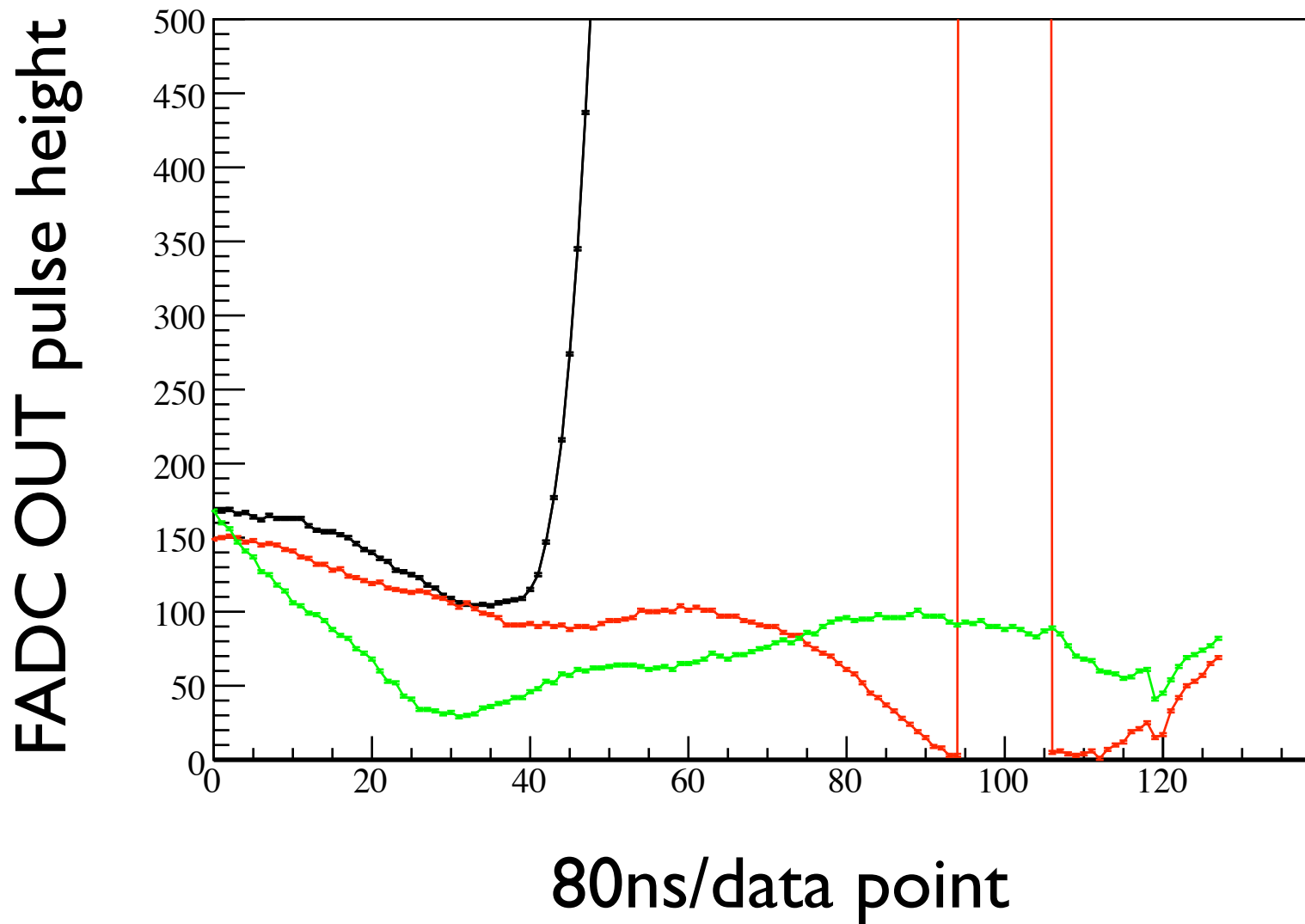
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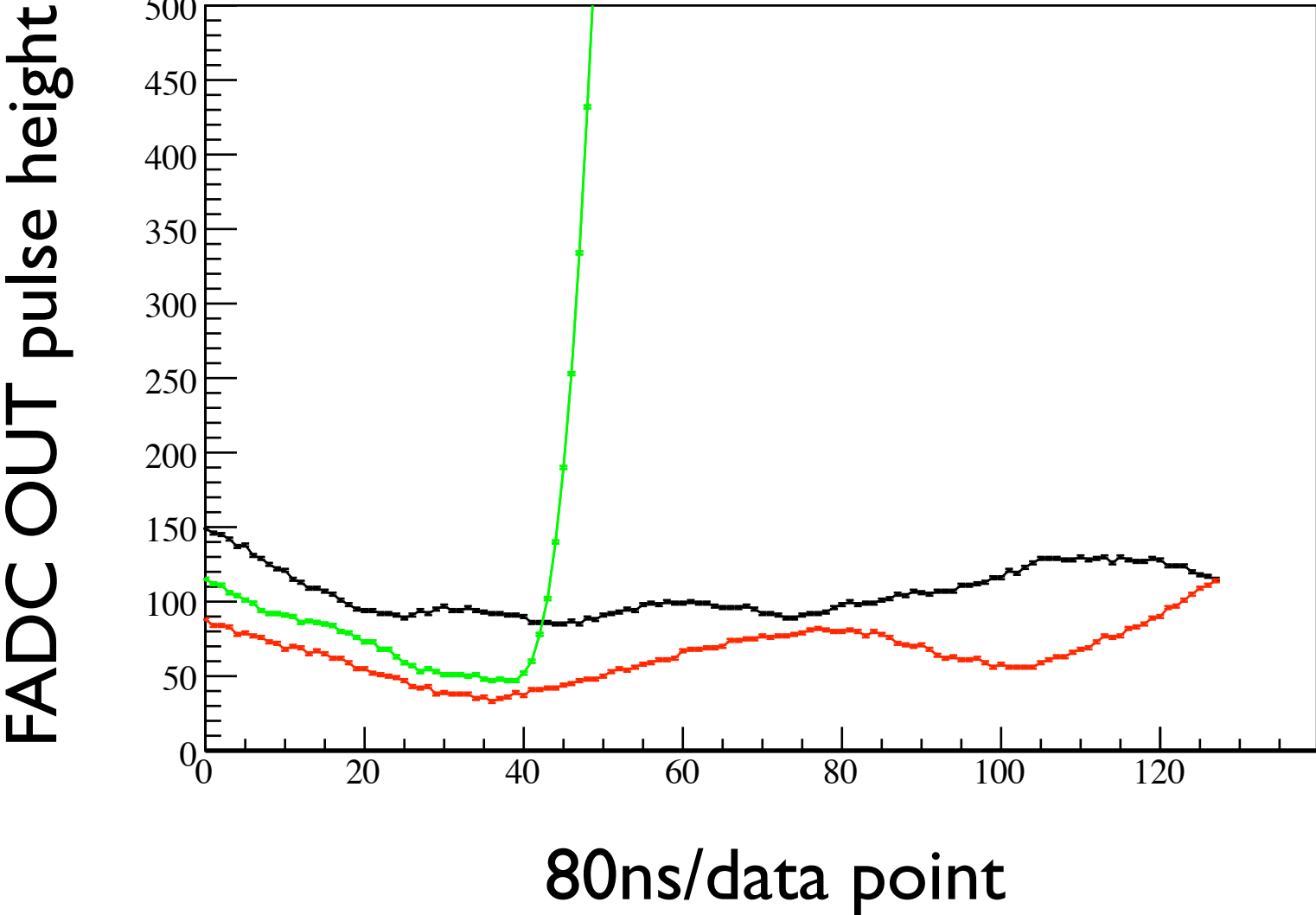
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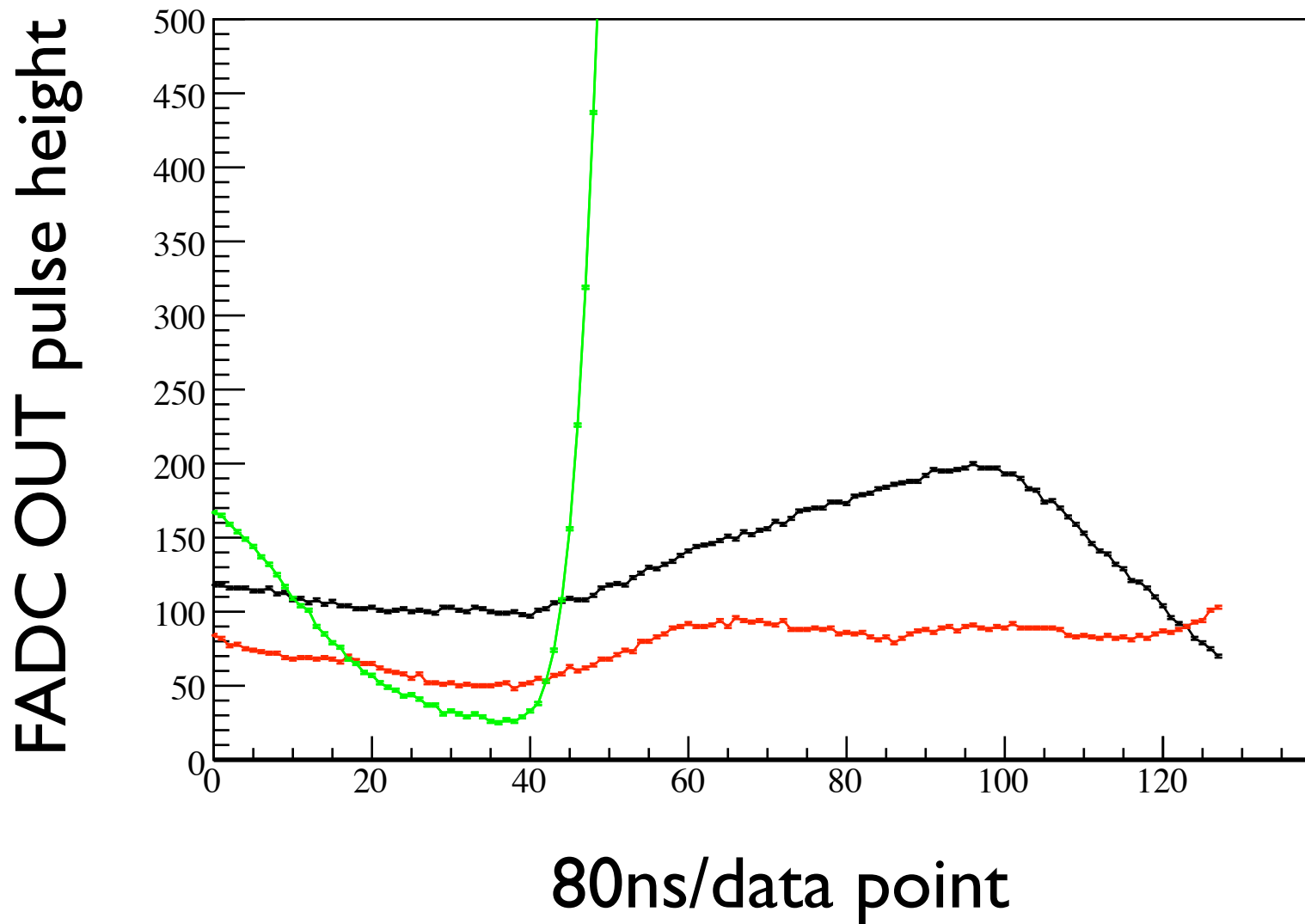
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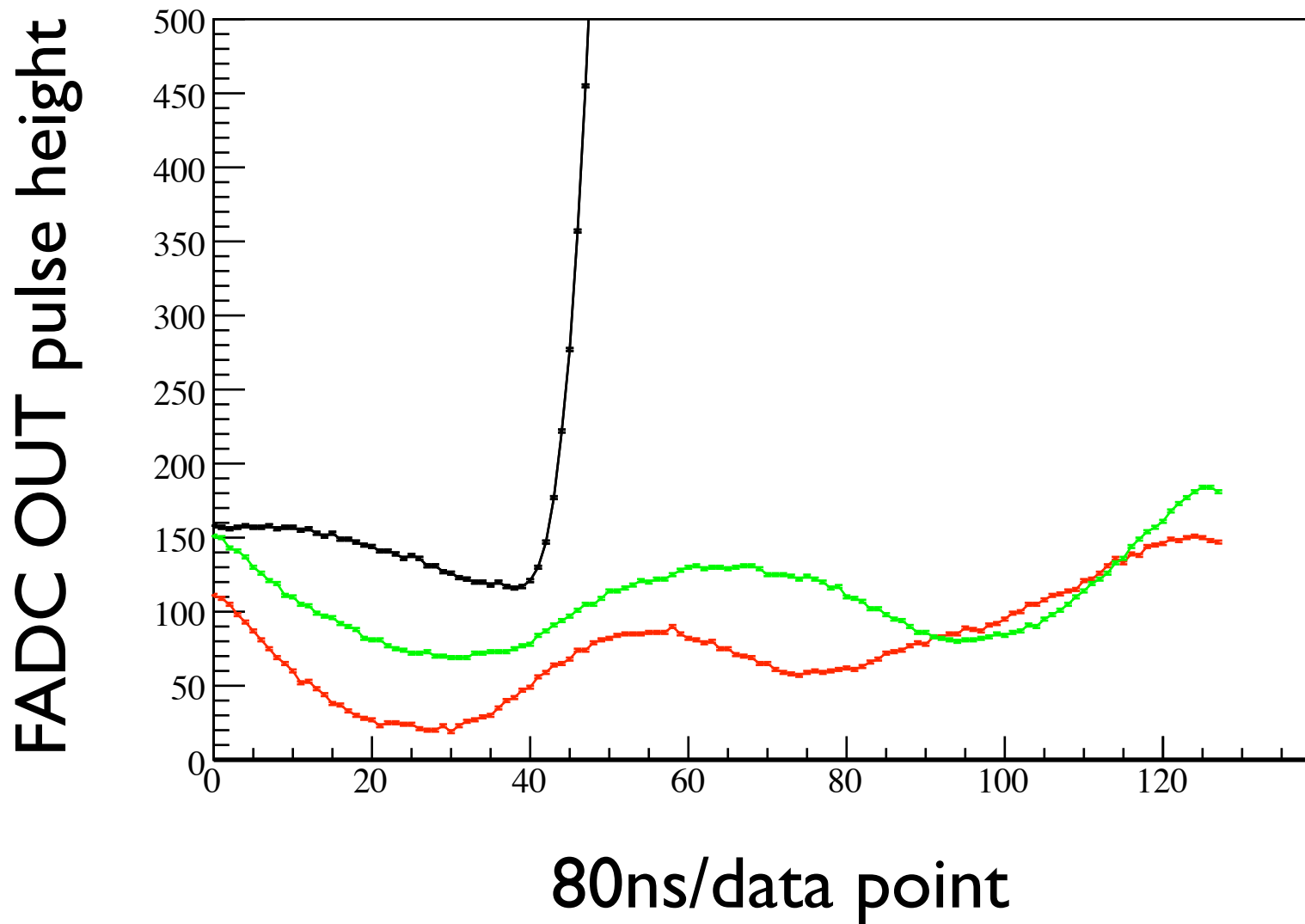
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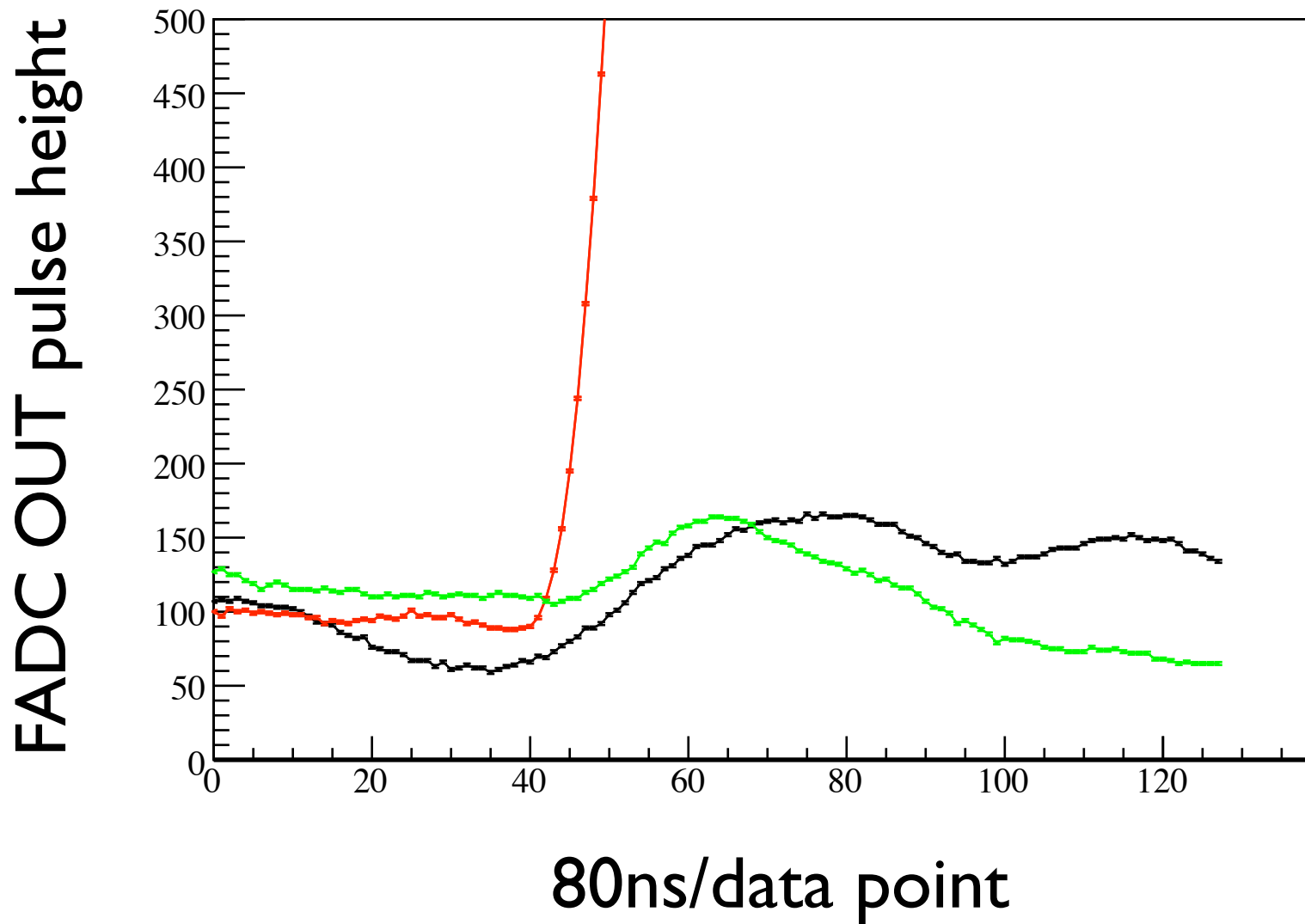
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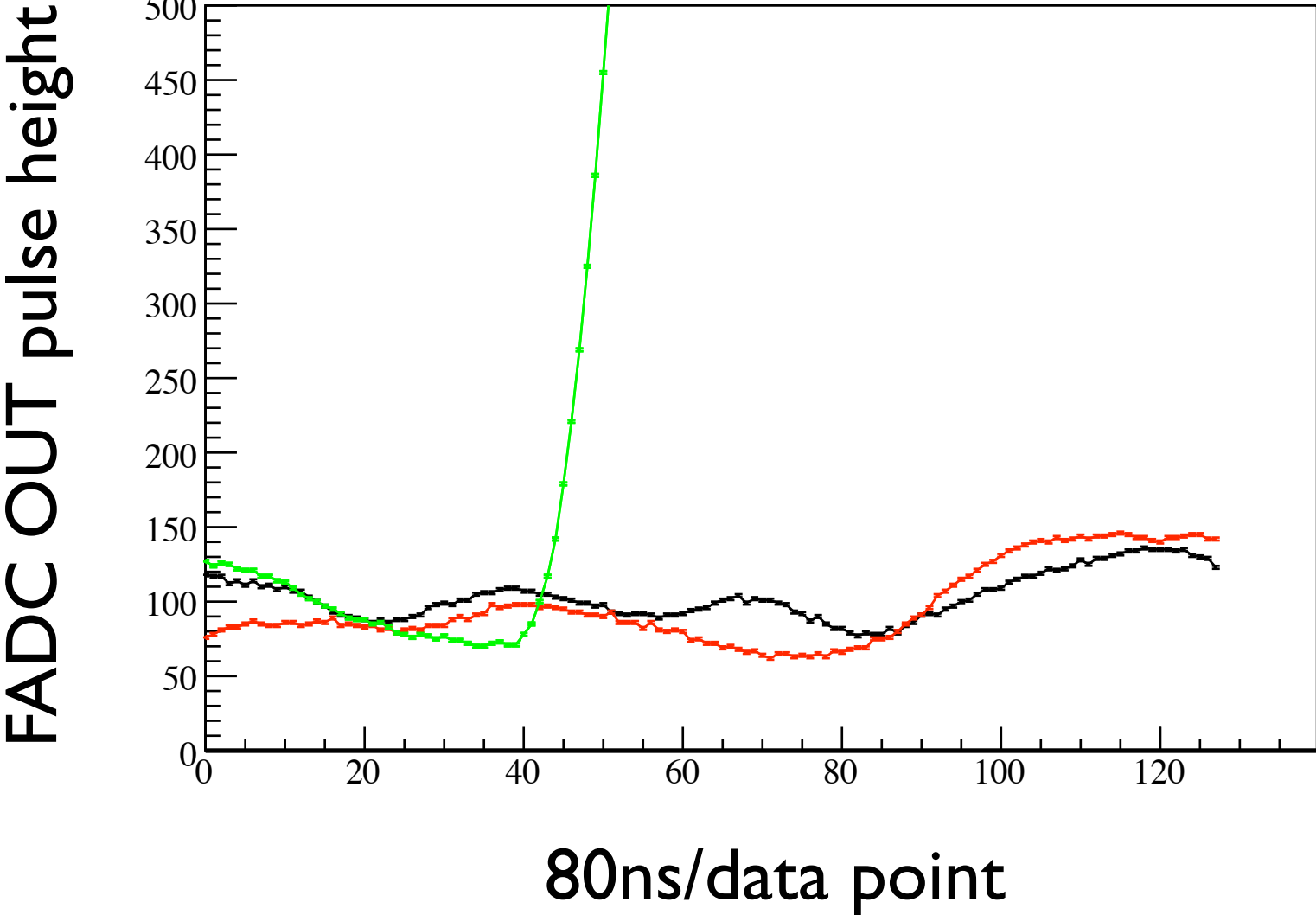
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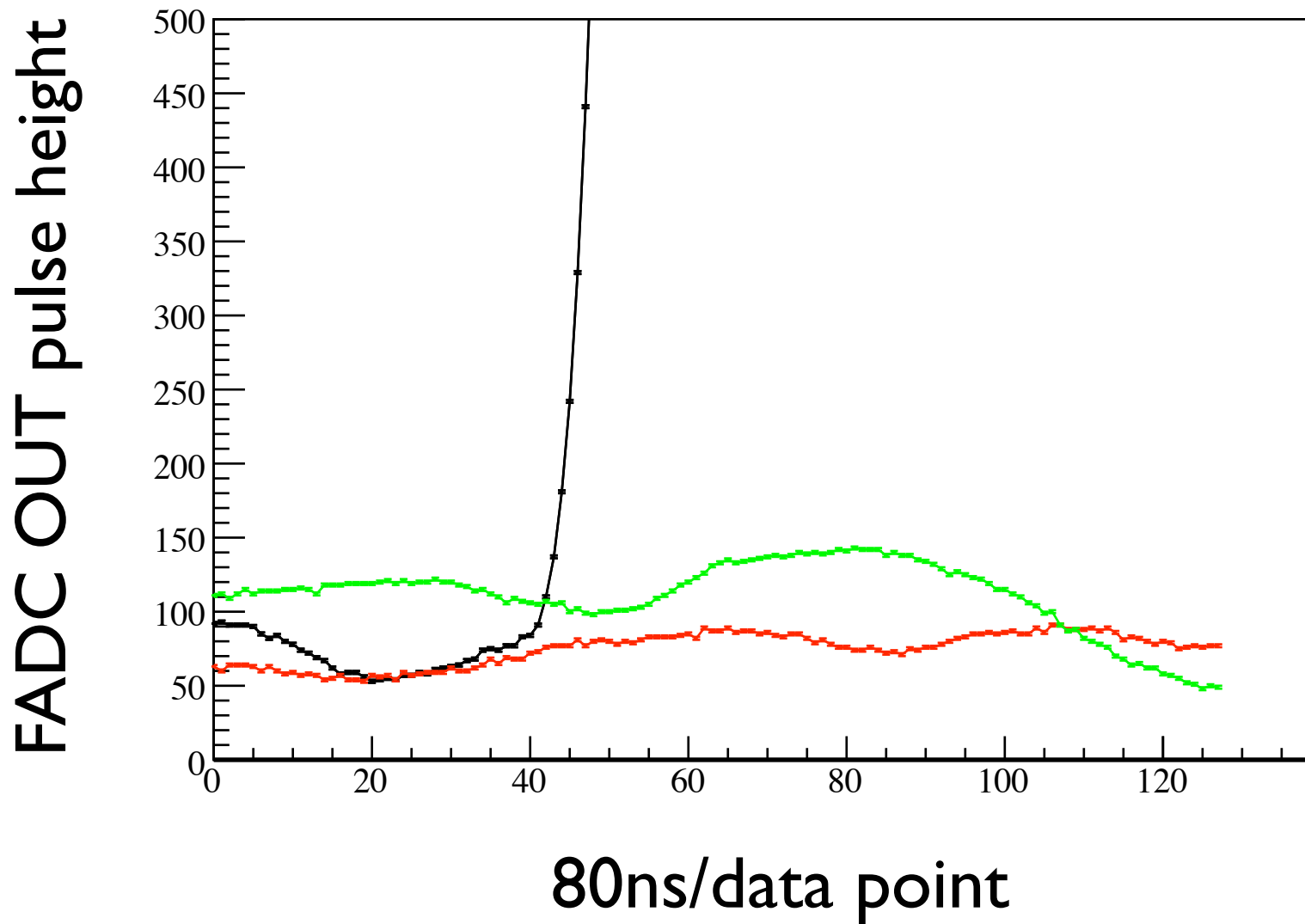
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fadc out run300 sdd5 ev3241



fadc out run300 sdd2 ev3244



fadc out run300 sdd4 ev3261

